



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In application of: N. FUKASAWA et al.

Serial Number: 09/029,608

Filed: May 15, 1998

Group Art Unit: 2814

Examiner: D. Graybill

For: METHOD AND MOLD FOR MANUFACTURING SEMICONDUCTOR DEVICE,  
SEMICONDUCTOR DEVICE, AND METHOD FOR MOUNTING THE DEVICE

**INFORMATION DISCLOSURE STATEMENT**  
**PURSUANT TO 37 CFR 1.97(b)**

Commissioner for Patents  
Washington, D.C. 20231

July 1, 2002

Sir:

The attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached Form PTO-1449. One copy of each of the documents cited in the search report and not yet made of record is attached along with a Partial European Search Report. The listed documents not cited in the Search Report were cited in related divisional application Serial No. 09/635,124 filed August 8, 2000 or 09/766,656, filed January 23, 2001.

No fee or certification is required in connection with this Information Disclosure Statement, since it is being submitted prior to the issuance of a first official action on the merits or expiration of the three month period following the filing date or the entry of the national stage of the above-captioned application.

The above information is presented so that the Patent and Trademark Office can, in the first instance, determine any materiality thereof to the claimed invention. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the

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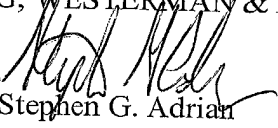
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documents cited in the attached Form PTO-1449 be made of record therein and appear on the first page of any patent to issue therefrom.

The Commissioner is authorized to charge our Deposit Account No. 01-2340 for any fee which is deemed by the Patent and Trademark Office to be required to effect consideration of this statement.

Respectfully Submitted,

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**23850**

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Enclosures: PTO-1449, Search Report and references (5)